

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/059,644
Filing Date April 13, 1998
Inventor Pai-Hung Pan
Assignee Micron Technology, Inc.
Group Art Unit 2822
Examiner Trinh, Michael
Attorney's Docket No. MI22-898
Title: Semiconductor Processing Methods of Forming a Conductive Gate and Line

INFORMATION DISCLOSURE STATEMENT

To: Assistant Commissioner for Patents
Washington, D.C. 20231

From: Jennifer J. Taylor, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Dear Sir:

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449 and copies of which are attached.

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: February 5, 2003

By:

Jennifer J. Taylor, Ph.D.
Reg. No. 48,711